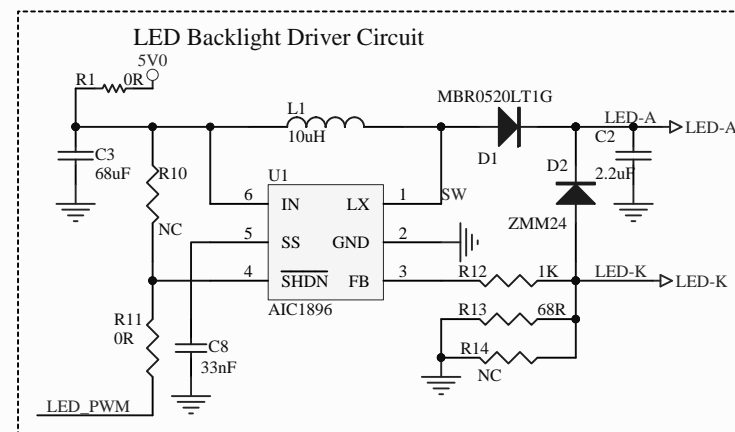
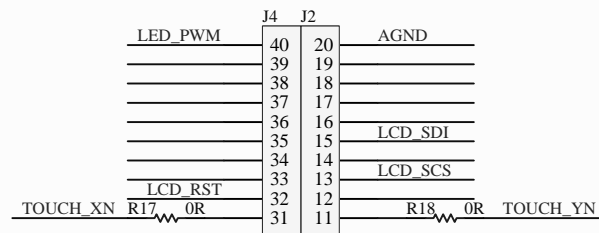
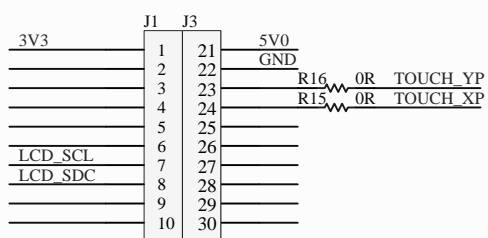


PS0	Interface Mode	
0	4-wire, 8-bit SPI interface (SCS, SCL, SDI, SDC)	Default
1	3-wire, 9-bit SPI interface (SCS, SCL, SDI)	R9=>R8 R3=>R2



Title			BOOSTXL-K350QVG-S1 circuit diagram		
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A4				03	
Date:	2014/12/12			Sheet of	
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